

In the Specification:

At page 6, lines 4-5, please amend the following:

“Other embodiments are possible where the DRAM is placed on a motherboard rather than than on a memory module.”

Also, please insert the following paragraph immediately preceding the paragraph beginning at line 14 on page 11. This added paragraph merely explains the added element in claim 5 that was required by the examiner. No new matter is added.

“The cache data 530 may include cache lines such as cache line 532 that are segmented into two sections A and B, where each of the sections A and B make up half of a full-length cache line. As discussed above in connection with Figure 4, the cache line segments may be addressed using a tag address that identifies the full-length cache line and two valid bits.”